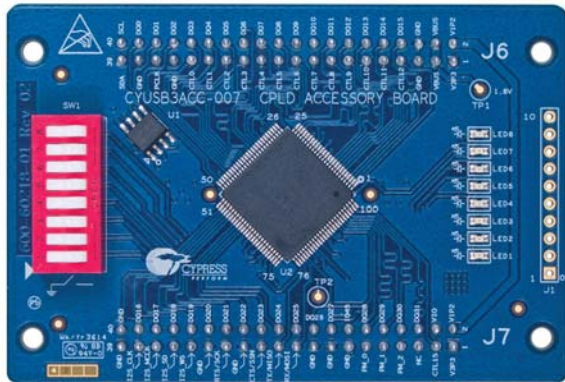


CYUSB3ACC-007 CPLD Accessory Board for the EZ-USB® FX3™ SuperSpeed Explorer Kit


Last Updated: Feb 17, 2015

The CYUSB3ACC-007 CPLD Accessory Board is designed to work with the GPIF II of the EZ-USB® FX3™ SuperSpeed Explorer Kit (CYUSB3KIT-003). It is meant to be used for the examples mentioned in the book SuperSpeed Device Design By Example, by John Hyde.

Guidelines to use this board with the FX3 SuperSpeed Explorer Kit can be found in the following book: SuperSpeed Device Design By Example, by John Hyde. For any questions regarding this board, please contact [fx3book@cypress.com](mailto:fx3book@ Cypress.com)



Related Files

File Title	Language	Size	Last Updated
 CYUSB3ACC-007 HW Files	English	4.16 MB	09/04/2014

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Related Resources

Code Examples

USB SuperSpeed Code Examples 03/12/2015

Development Kits/Boards

CYUSB3KIT-003 EZ-USB® FX3™ SuperSpeed Explorer Kit 09/23/2015

CYUSB3ACC-004 Aptina™ Image Sensor Interconnect Board for the EZ-USB® FX3™ SuperSpeed Explorer Kit 10/06/2014

CYUSB3ACC-005 FMC Interconnect Board for the EZ-USB® FX3™ SuperSpeed Explorer Kit 10/06/2014

CYUSB3ACC-006 HSMC Interconnect Board for the EZ-USB® FX3™ SuperSpeed Explorer Kit 10/06/2014